

ABSTRACT

A method is provided to perform spatial locality testing on a memory array
5 having a logical address map distinct from its physical address map. The built-in self-
test generator performs memory spatial locality tests on the memory array by
generating adjacent physical memory row addresses that are then converted to
corresponding logical memory row addresses. Once the physical memory row
address is converted to its corresponding memory row address the test vector is
10 written to the logical memory row address to perform spatial locality tests on adjacent
physical memory row addresses of the memory array.